DMCS21

M.Sc. DEGREE EXAMINATION, JUNE/JULY - 2019

(Second Year)

COMPUTER SCIENCE

User Interface Design

Time : 3 Hours   Maximum Marks : 70

SECTION – A

(3 x 15 = 45)

Answer any THREE Questions from the following

Q1)   Discuss usability requirements and usability motivations of interactive systems.

Q2)   Explain three pillar approach of user interface development.

Q3)   Explain in detail about dialog boxes and types.

Q4)   Discuss various presentation controls and their usage.

Q5)   Write about the working with bit maps and icons.

SECTION – B

(5 x 4 = 20)

Answer any FIVE Questions from the following

Q6)   Explain about Object action interface model.

Q7)   What are the merits and demerits of "drag and drop operation" with illustrations?

Q8)   List the states of windows and explain them.

Q9)   Compare menus and dialog boxes.

Q10)   Write about various "Cursor hinting" techniques use.

Q11) What is meant by orchestration and flow presentations?

Q12) Write about additive and group selection methods.

Q13) Explain idiocy and task coherence.

SECTION – C

(5 x 1 = 5)

<u>Answer ALL questions</u>

Q14) What is the goal of directed design?

Q15) What are the alerts?

Q16) Differentiate undo and redo operations.

Q17) What is Gizmos?

Q18) What are the meta keys?

DMCS22

M.Sc. DEGREE EXAMINATION, JUNE/JULY - 2019

(Second Year)

COMPUTER SCIENCE

Computer Graphics

Time : 3 Hours   Maximum Marks : 70

SECTION – A

(3 x 15 = 45)

Answer any THREE Questions

Q1)   a)   Explain the working principle of various graphic input devices with neat sketches.

b)   Demonstrate line from (20, 10) to (30, 18) with all necessary calculation using DDA.

Q2)   Explain 8-connected boundary filled and polygon filled algorithms with examples.

Q3)   Clip the line PQ having coordinates P (4, 1) and Q (6, 4) against the clip window having vertices A (3, 2), B (7, 2), C (7, 6) and D (3, 6) using Cohen Sutherland line clipping algorithm. Mention the limitations of algorithm. How it can be overcome?

Q4)   What are the classification of the visible surface detection algorithms? Discuss any two.

Q5)   a)   Find the composite transformation matrix for mirror reflection of a 3D object with respect to the plane passing through the origin and having a normal vector whose direction is $N = I + J + K$.

b)   Derive the equations for parallel projections.

SECTION – B

(5 x 4 = 20)

Answer any FIVE Questions from the following

Q6)   Differentiate raster scan display and random scan display.

Q7)   Explain odd-even method of determining polygon inside points.

Q8)   Prove that two successive 2-D rotation are additive (i.e. $R(\theta_1) . R(\theta_2) = R(\theta_1 + \theta_2)$)

Q9)   Explain the terms Window and viewport with reference to 2-D displays.

Q10)   In translate a triangle with vertices at original coordinates (10, 20), (10, 10), (20, 10) by $t_x =5$, $t_y =10$, compute the resultant coordinate of the triangle.

Q11) Briefly explain about interactive picture construction techniques.

Q12) Write down 3 – D rotation matrices.

Q13) Explain Back face detection method in brief.

SECTION – C

(5 x 1 = 5)

Answer ALL questions

Q14) Define resolution.

Q15) What is Shearing?

Q16) Define antialiasing.

Q17) Define composite transformation.

Q18) What is meant by viewing pipelining?

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DMCS23

M.Sc. DEGREE EXAMINATION, JUNE/JULY - 2019
(Second Year)
COMPUTER SCIENCE
Object Oriented Analysis and Design

Time : 3 Hours   Maximum Marks : 70

SECTION – A

(3 x 15 = 45)

Answer any THREE Questions from the following.

Q1)   a)   Explain basic principles of modeling.

       b)   Draw the architecture of a software-intensive system and explain each element.

Q2)   Draw the complete use case diagram for the library system and explain the relationships and responsibilities of various actors.

Q3)   What is the purpose of deployment diagram? Give one example. How are these different from collaboration diagrams?

Q4)   Describe the strategies used to identify the conceptual classes. Describe the steps to create domain model used for representing the conceptual classes.

Q5)   Explain the forward engineering tool and reverse engineering tool for a sample code with respect to the state chart diagram.

SECTION – B

(5 x 4 = 20)

Answer any FIVE Questions from the following.

Q6)   Describe various behavioral things.

Q7)   Write about aggregation and composition with example.

Q8)   Briefly explain about Stereotypes and Tagged Values.

Q9) Compare and Contrast is-a relationship with has-a relationship.

Q10) Illustrate static and dynamic model of a system.

Q11) Explain about forking and joining concepts in activity diagram with an example.

Q12) What is an event? What are different types of events?

Q13) Write short notes on object oriented languages.

SECTION – C

(5 x 1 = 5)

Answer ALL questions.

Q14) What is an artifact?

Q15) What are interaction diagrams?

Q16) Distinguish signals and active classes.

Q17) What is meant by high cohesion?

Q18) State 100% rule.

DMCS24A

M.Sc. DEGREE EXAMINATION, JUNE/JULY - 2019
Second Year
COMPUTER SCIENCE
Advanced Computer Architecture

Time : 3 Hours   **Maximum Marks : 70**

SECTION – A

(3 x 15 = 45)

Answer any three questions from the following

Q1) Explain about the pipeline Computers and Array   Computers.

Q2) Explain about Illiac - IV system architecture.

Q3) Explain about the SIMD array processors in detail.

Q4) Explain about the classification of multiprocessor operating systems.

Q5) Explain about the Cray X - MP architecture.

SECTION – B

(5 x 4 = 20)

Answer any five questions from the following

Q6) Explain about the parallel processing applications.

Q7) Explain about the characteristics of Vector processing.

Q8) Explain about the specifications of AP-120B.

Q9) Explain about Parallel Memory Allocation.

Q10) Explain about the Time - shared bus system.

Q11) Explain about the dimensions of multiprocessor management.

Q12) Explain about different types of Multiprocessor Systems.

Q13) Explain about the dataflow computers.

SECTION – C

(5 x 1 = 5)

<u>Answer all questions</u>

Q14) What is the advantages using instruction prefetch.

Q15) What is SIMD array processor?

Q16) What is the difference between Static and Dynamic Network.

Q17) What is processor Synchronization?

Q18) What is the difference between control flow and data flow computers.

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DMCS24B

M.Sc. DEGREE EXAMINATION, JUNE/JULY - 2019
Second Year
COMPUTER SCIENCE
Microprocessor & Applications

Time : 3 Hours   **Maximum Marks : 70**

SECTION – A

(3 x 15 = 45)

Answer any three questions from the following

Q1) Explain about 8086 internal architecture with a neat block diagram.

Q2) Explain about 8086 athematic instructions with   examples.

Q3) Explain about interrupt driven I/O transfer with a neat flow chart.

Q4) Explain about process management in iRMX 86.

Q5) Explain about iRMX 86 semaphore operations.

SECTION – B

(5 x 4 = 20)

Answer any five questions from the following

Q6) Explain about general operations of a computer.

Q7) Explain about different types of data representation.

Q8) Explain about 8086 LOOP instructions.

Q9) List different types of flags available in 8086 and write their functions.

Q10) Explain about linking.

Q11) Explain about 8086 procedures.

Q12) Explain about multiprogramming.

Q13) Explain about virtual memory.

SECTION – C

(5 x 1 = 5)

Answer all questions

Q14) What is an effective address?

Q15) What is the purpose of NOP instruction.

Q16) What is an interrupt.

Q17) What is a process.

Q18) List key features of iRMX 86.

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DMCS25A

M.Sc. DEGREE EXAMINATION, JUNE/JULY - 2019
(Second Year)
COMPUTER SCIENCE
Cryptography and Network Security TCP/IP

Time : 3 Hours   Maximum Marks : 70

SECTION – A

(3 x 15 = 45)

Answer any THREE Questions from the following

Q1)   Discuss about different security mechanisms and security services.

Q2)   Explain Data Encryption standard (DES) in detail.

Q3)   How message authentication code can be used to achieve message authentication and confidentiality? Explain.

Q4)   Describe the process involved in digital signatures and explain digital signature algorithm.

Q5)   What does authentication header provide in case of IP security? Explain the various fields in Authentication Header.

SECTION – B

(5 x 4 = 20)

Answer any FIVE Questions from the following

Q6)   State and explain different types of attacks.

Q7)   Encrypt the message "Exam" using the Hill cipher with the key .

Q8)   Write the process of AES encryption with neat diagram.

Q9)   Explain avalanche effect in DES and discuss strength of DES in brief.

Q10) How are keys generated in Cast-128 algorithm?

Q11) Calculate cipher text in case of RSA if p=3, q=11, e=3, M=5.

Q12) What characteristics are needed in a secure hash function?

Q13) What steps sending PGP (pretty good privacy) perform? Explain PGP message generation.

SECTION – C

(5 x 1 = 5)

<u>Answer ALL questions</u>

Q14) Define authentication.

Q15) What is mono alphabetic cipher.

Q16) What is a dual signature?

Q17) Give the difference between public and private key cryptography.

Q18) What is Firewall?

DMCS25B

M.Sc. DEGREE EXAMINATION, JUNE/JULY - 2019
Second Year
COMPUTER SCIENCE
TCP/IP

Time : 3 Hours   **Maximum Marks : 70**

SECTION – A

(3 x 15 = 45)

Answer any three questions from the following

Q1) Explain different types of IP addressing in detail.

Q2) Discuss the ICMP protocol.

Q3) Explain about User Datagram Protocol.

Q4) Explain about BOOTP protocol.

Q5) Discuss about TELNET protocol.

SECTION – B

(5 x 4 = 20)

Answer any five questions from the following

Q6) Explain about switched WANs.

Q7) Explain about subnetting.

Q8) Compare between direct and indirect delivery of IP packets.

Q9) Write in detail about RARP.

Q10) Give in detail the different types of ICMP messages.

Q11) Explain about TCP congestion control.

Q12) Explain about MBONE.

Q13) Explain about BGP.

SECTION – C

(5 x 1 = 5)

<u>Answer all questions</u>

Q14) What is a LAN.

Q15) Define fragmentation.

Q16) What is a Silly Window Syndrome.

Q17) What is Multicast tree.

Q18) What is Rlogin.

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DMCS26

M.Sc. DEGREE EXAMINATION, JUNE/JULY - 2019

(Second Year)

COMPUTER SCIENCE

Dataware Housing and Datamining

Time : 3 Hours   Maximum Marks : 70

SECTION – A

(3 x 15 = 45)

Answer any THREE Questions from the following

Q1) Discuss data warehouse design methodologies.

Q2) Explain about different elements of ETL architecture.

Q3) What is data cube? Explain different operations on data cube.

Q4) Write about working oracle data warehouse builder.

Q5) Draw and explain the data mining architecture and discuss major issues in data mining.

SECTION – B

(5 x 4 = 20)

Answer any FIVE Questions from the following

Q6) Explain how the Data Warehouse different from relational database.

Q7) Write about star schema design issues.

Q8) Explain the role of 'metadata repository' in a data warehouse.

Q9) Describe the activities of ETL process.

Q10) Describe Data granularity in Data Warehouse.

Q11) Write the components of a data warehouse snapshot.

Q12) What is OLAP? Give the difference between OLAP and OLTP.

Q13) Write about association rule mining.

SECTION – C

(5 x 1 = 5)

<u>Answer all questions</u>

Q14) What is data mart?

Q15) Define clustering.

Q16) Define Enterprise Warehouse.

Q17) What is Virtual Warehouse?

Q18) What is 'data discretization'?

Total No. of Questions : 18]

DMCS27A

M.Sc. DEGREE EXAMINATION, JUNE/JULY - 2019
Second Year
COMPUTER SCIENCE
Embedded Systems

Time : 3 Hours   Maximum Marks : 70

SECTION – A

(3 x 15 = 45)

Answer any three questions from the following

Q1) Discuss about the embedded system design challenges with reference to Telegraph system.

Q2) Explain the term 'interrupt Latency'. Discuss various measures to reduce the interrupt latency in embedded applications.

Q3) Discuss about the Round - Robin - with - interrupts with an example.

Q4) Explain about Timer functions and Events.

Q5) Discuss about various laboratory tools used for   embedded system debugging.

SECTION - B

(5 x 4 = 20)

Answer any five questions from the following

Q6) Explain about ROM.

Q7) Explain about the Signal Loading.

Q8) Explain about Wait signals and Wait states.

Q9) Explain about PAL.

Q10) Explain about shortcomings in round robin architecture.

Q11) Explain about semaphore problems.

Q12) What is best plan for testing of an embedded system?

Q13) Explain about software only monitors.

SECTION – C

(5 x 1 = 5)

Answer all questions

Q14) What is FPGA?

Q15) How does the scheduler know when a task has become blocked or unblocked?

Q16) If two interrupts happen at the same time, which interrupt routine does the microprocessor do first?

Q17) What is Test Scaffold Code?

Q18) Explain about the getting "Visibility" into the Hardware.

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DMCS27B

M.Sc. DEGREE EXAMINATION, JUNE/JULY - 2019
Second Year
COMPUTER SCIENCE
Image Processing

Time : 3 Hours   Maximum Marks : 70

SECTION – A

(3 x 15 = 45)

Answer any three questions from the following

Q1) Describe various components of image processing system.

Q2) Explain the basic concepts of sampling and quantization in the generation of digital image.

Q3) What is Histogram of an Image? Explain the concept of Histogram Equalization technique for Image enhancement.

Q4) Discuss different image compression models.

Q5) Illustrate threshold based and region based image segmentation with example.

SECTION – B

(5 x 4 = 20)

Answer any FIVE questions from the following

Q6) Explain about image acquisition using a circular sensor strip.

Q7) State the relationships between pixels.

Q8) Explain the concept of wavelet packets and write its advantages.

Q9) Compute the Haar transform of the 2 × 2 image

Q10) What is error free image compression.

Q11) Explain the duality of erosion and dilation operations.

Q12) Explain the effect of noise on edge detection.

Q13) What is meant by edge linking? Explain edge linking using local processing.

SECTION - C

(5 x 1 = 5)

Answer all questions

Q14) Define Walsh Transform.

Q15) What is meant by pixel depth?

Q16) Define high boost filter.

Q17) Define Fourier spectrum.

Q18) What is the concept of histogram equalization?

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DMCS28A

M.Sc. DEGREE EXAMINATION, JUNE/JULY - 2019
Second Year
COMPUTER SCIENCE
ARTIFICIAL INTELLIGENCE

Time : 3 Hours   Maximum Marks : 70

SECTION – A

(3 x 15 = 45)

Answer any three questions

Q1) a)   Determine the start state, goal state and legal moves and draw the state space diagram for the well - known water jug problem listed below:
"Given two water jugs of 4 liters and 3 liters' capabilities, neither have any measuring marks on it. There is pump that can be used to fill the jugs. How can you get exactly two liters of water into 4-liter jug"

   b)   How are production system and control strategies applied in solving AI problems?

Q2) Solve the following Crypt Arithmetic Problem:
SEND + MORE = MONEY

Q3) What is matching? Describe different matching   techniques with example.

Q4) a)   Draw a Conceptual dependency form of the following statements:
   i)   John gave a coin to the beggar.
   ii)   John ate ice-cream with a spoon.

   b)   Explain semantic nets with example.

Q5) Explain the architecture of expert system and also give working of expert system shell.

SECTION – B

(5 x 4 = 20)

Answer any five questions

Q6) Describe production system characteristics.

Q7) Explain the term local maxima and plateau in Hill climbing.

Q8) Assume the facts:

i)   Steve only likes easy courses
ii) Science courses are hard.
iii) All courses in the arts department are easy.
iv) TL_301 is an arts department course.

Use resolution to answer the question which course would Steve like?

Q9) Differentiate procedural knowledge and declarative knowledge.

Q10) Explain Dependency directed back tracking.

Q11) Describe the components of script.

Q12) Write short notes on common sense ontologies.

Q13) Write about semantic and pragmatic analysis.

SECTION – C

(5 x 1 = 5)

Answer all questions

Q14) Define heuristic search.

Q15) Define Well – formed formulae.

Q16) Define Non – monotonic reasoning.

Q17) List any two examples of expert systems.

Q18) What is AND – OR graph?

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DMCS28B

M.Sc. DEGREE EXAMINATION, JUNE/JULY - 2019
Second Year
COMPUTER SCIENCE
Compiler Design

Time : 3 Hours   **Maximum Marks : 70**

SECTION – A

(3 x 15 = 45)

Answer any three questions

Q1) a)   How does lexical analyzer help in the process of compilation? Consider the following   Conditional statement : "if $(x > 3)$ then $y = 5$ else $y = 10$;"

      b)   Construct DFA for the regular expression $(a + b)^*ab(a + b)^*$

Q2) Design canonical parser for the following grammar :
    $S \rightarrow CC, C \rightarrow cC|d$

Q3) Explain Quadruple, triple, and indirect triple with   suitable example.

Q4) What is syntax directed definition and syntax directed translation? Discuss.

Q5) Generate machine code for the following instruction and also compute its cost $v = a + (b * c) - d$.

SECTION – B

(5 x 4 = 20)

Answer any five questions

Q6) Write about Boot strapping.

Q7) Draw transition diagram for relational operators.

Q8) Write about operator precedence parsing technique with example.

Q9) What is left recursion? Eliminate left recursion from the following grammar :
    $S \rightarrow (L) |a, L \rightarrow L, S | S$

Q10) Draw a DAG for expression : a + a* (b – c) + (b – c)*d.

Q11) Describe how the symbol table organized.

Q12) Explain how type checking and error reporting is performed in compiler.

Q13) Write short notes on Peephole optimization.

SECTION – C

(5 x 1 = 5)

Answer all questions

Q14) What is meant by handle pruning?

Q15) Define NFA and DFA.

Q16) Define sentential form.

Q17) What is meant by code motion?

Q18) Define 1 – attributes and s – attributes.

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